

# Hyperfast Rectifier, 4 A FRED Pt®



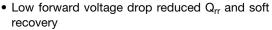


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PRODUCT SUMMARY				
Package	TO-252AA (D-PAK)			
I <sub>F(AV)</sub>	4 A			
V <sub>R</sub>	200 V			
V <sub>F</sub> at I <sub>F</sub>	0.71 V			
t <sub>rr</sub> (typ.)	23 ns			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			

#### **FEATURES**

- Hyperfast recovery time
- 175 °C max. operating junction temperature
- · Output rectification freewheeling





FREE

- · Low leakage current
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	$V_{RRM}$		200	V		
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 164 °C	4			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	80	Α		
Peak repetitive forward current	I <sub>FM</sub>	T <sub>C</sub> = 164 °C, f = 20 kHz, d = 50 %	8			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ		-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 4 A	-	0.87	0.95	V	
		I <sub>F</sub> = 4 A, T <sub>J</sub> = 150 °C	-	0.71	0.80		
Doverno logicado quirrent	-	V <sub>R</sub> = V <sub>R</sub> rated	-	-	3		
Reverse leakage current I <sub>R</sub>		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	2	20	μA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	17	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 8 -		nH			



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	23	-	
Reverse recovery time	+	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		ı	24	1	
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	$I_F = 4 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	20	-	ns
		T <sub>J</sub> = 125 °C		-	27	-	
Dook receivent ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	2	-	Α
Peak recovery current		T <sub>J</sub> = 125 °C		-	3.4	-	_ A
Poverce receivery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-	nC
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	46	-	] IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to case per leg	$R_{thJC}$		-	2.7	3.2	°C/W
Approximate weight				0.3		g
Approximate weight				0.01		oz.
Marking device	·	Case style TO-252AA (D-PAK)		4EWH	02FNH	

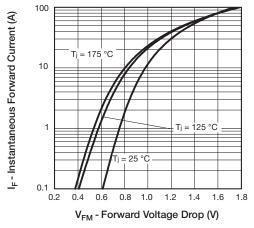


Fig. 1 - Typical Forward Voltage Drop Characteristics

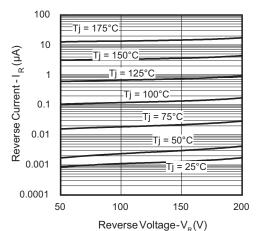


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

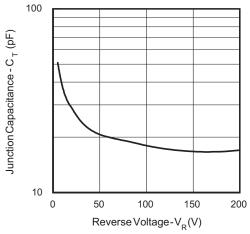


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

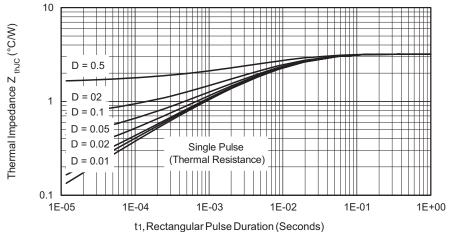


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

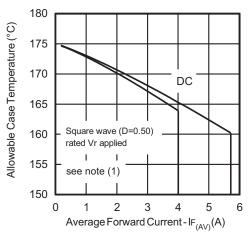


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

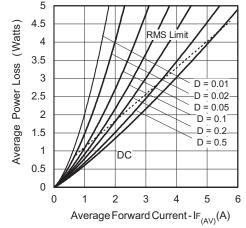


Fig. 6 - Forward Power Loss Characteristics

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# Vishay Semiconductors

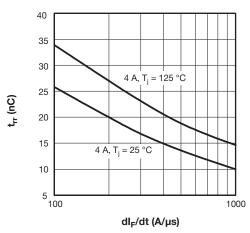


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_{\mbox{\scriptsize F}}/dt$ 

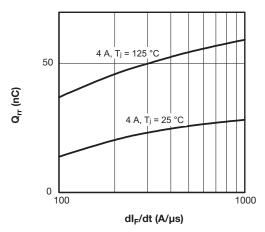
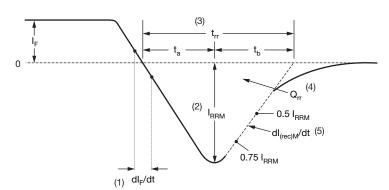


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  ${\rm Q}_{\rm rr}$  area under curve defined by  ${\rm t}_{\rm rr}$  and  ${\rm I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

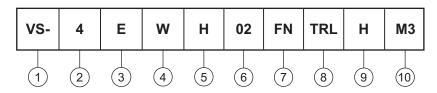
(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (4 = 4 A)

Circuit configuration:

E = single diode

4 - Package identifier:

W = D-PAK

5 - H = hyperfast recovery

Voltage rating (02 = 200 V)

7 - FN = TO-252AA

8 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-4EWH02FNHM3	75	3000	Antistatic plastic tube			
VS-4EWH02FNTRHM3	2000	2000	13" diameter reel			
VS-4EWH02FNTRLHM3	3000	3000	13" diameter reel			
VS-4EWH02FNTRRHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95519</u>					
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				
SPICE model	www.vishay.com/doc?95381				



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